

# APPROVAL SHEET

MULTILAYER CERAMIC CAPACITORS
Microwave Series+ High Reliability (RH)
0402 to 0805 Sizes (200V to 250V)
NP0 Dielectric

**RoHS Compliance** 

\*Contents in this sheet are subject to change without prior notice.



### 1. INTRODUCTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

WTC RH series MLCC are used at high frequencies generally and have a small temperature coefficient of capacitance, typical within the ±30ppm/℃ required for NP0 (C0G) classification and have internal electrodes of excellent conductivity. Thus, WTC RH series MLCC will have the feature of low ESR and high Q characteristics.

WTC RH series have high reliability characteristic, life test condition: 150°C / 2000hrs / 2xRated Voltage.

### 2. FEATURES

- b. High Q and low ESR performance at high frequency.
- c. Ultra low capacitance to 0.1pF.
- d. Ultra high reliability (150<sup>o</sup>C / 2000hrs / 2x Rated Voltage).
- e. Can offer high precision tolerance to ±0.05pF.
- f. Quality improvement of telephone calls for low

### 3. APPLICATIONS

- Telecommunication products & equipments: Mobile phone, WLAN, Base station, Small cell.
- b. RF module: Power amplifier, VCO.
- c. Tuners.
- d. High quality concern wireless device.

### 4. HOW TO ORDER

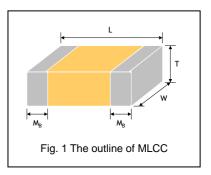
<u>RH</u>	<u>21</u>	<u>N</u>	<u>100</u>	<u>J</u>	<u>251</u>	<u>C</u>	Ī
<u>Series</u>	Size	<u>Dielectric</u>	Capacitance	Tolerance	Rated voltage	<u>Termination</u>	<u>Packaging</u>
RH=High reliability & Ultra High Q & Low ESR	15=0402 (1005) 18=0603 (1608) 11=0505 (1414) 21=0805 (2012)	<b>N</b> =NP0	Two significant digits followed by no. of zeros. And R is in place of decimal point.	A=±0.05pF B=±0.1pF C=±0.25pF F=±1% G=±2%	Two significant digits followed by no. of zeros. And R is in place of decimal point.	<b>C</b> =Cu/Ni/Sn	T=7" reeled G= 13" reeled
			eg.: 0R5=0.5pF 1R0=1.0pF 100=10x10 <sup>0</sup> =10pF	<b>J</b> =±5%	eg. : <b>201</b> =200 VDC <b>251</b> =250 VDC		



# **5. EXTERNAL DIMENSIONS**

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Sym	bol	Remark	M <sub>B</sub> (mm)
0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	#	0.25+0.05/-0.10
0603 (1608)	1.60±0.10	0.80±0.10	0.80±0.07	s		0.40±0.15
0805 (2012)	2.00±0.20	1.25±0.20	0.85±0.10	Т		0.50±0.20
0505 (1414)	1.40 +0.38/-0.25	1.40±0.38	1.15±0.15	J	#	0.25+0.25/-0.13

<sup>#</sup> Reflow soldering only is recommended.



# **6. GENERAL ELECTRICAL DATA**

Dielectric	NP0	
Size	0402, 0505, 0603, 0805	
Capacitance*	0.1pF to 10pF	
	Cap≤5pF: A (±0.05pF), B (±0.1pF), C (±0.25pF)	
Capacitance tolerance	5pF <cap<10pf: (±0.1pf),="" (±0.25pf)<="" b="" c="" th=""></cap<10pf:>	
	Cap=10pF: F (±1%), G (±2%), J (±5%)	
Rated voltage (WVDC)	200V, 250V	
Q*	Q≥800+20C	
Insulation resistance at Ur	≥10GΩ or RxC≥100Ω-F whichever is smaller.	
Operating temperature	-55 to +150℃	
Capacitance change	±30ppm/℃	
Termination	Ni/Sn (lead-free termination)	

<sup>\*</sup> Measured at the conditions of 25℃ ambient temper ature and 30~70% related humidity.

Apply 1.0±0.2Vrms, 1.0MHz±10% for Cap≤1000pF and 1.0±0.2Vrms, 1.0kHz±10% for Cap>1000pF.

# 7. PACKAGING DIMENSION AND QUANTITY

Cina	This language (many) (Complete	Thickness (mm)/Symbol		tape
Size	inickness (mm)/Symbo			13" reel
0402 (1005)	0.50±0.05	N	10,000	50,000
0603 (1608)	0.80±0.07	S	4,000	15,000
0805 (2012)	0.85±0.10	Т	4,000	15,000
Cina	Thinkmann (mm)/Sumb		Plastic	tape and the state of the state
Size	Thickness (mm)/Symbo	)I	7" reel	13" reel
0505 (1414)	1.15±0.15	J	3,000	-

Unit: pieces



# **8. CAPACITANCE RANGE**

	DIELECTRIC	NP0	
SIZE		0402	
RAT	ED VOLTAGE (VDC)	200	Tolerance
	0.1pF (0R1)	N	В
	0.2pF (0R2)	N	A, B
	0.3pF (0R3)	N	A, B
	0.4pF (0R4)	N	A, B
	0.5pF (0R5)	N	A, B, C
	0.6pF (0R6)	N	A, B, C
	0.7pF (0R7)	N	A, B, C
	0.8pF (0R8)	N	A, B, C
	0.9pF (0R9)	N	A, B, C
	1.0pF (1R0)	N	A, B, C
	1.1pF (1R1)	N	A, B, C
	1.2pF (1R2)	N	A, B, C
a	1.3pF (1R3)	N	A, B, C
Capacitance	1.4pF (1R4)	N	A, B, C
ita	1.5pF (1R5)	N	A, B, C
ga	1.6pF (1R6)	N	A, B, C
ğ	1.7pF (1R7)	N	A, B, C
	1.8pF (1R8)	N	A, B, C
	1.9pF (1R9)	N	A, B, C
	2.0pF (2R0)	N	A, B, C
	2.1pF (2R1)	N	A, B, C
	2.2pF (2R2)	N	A, B, C
	2.3pF (2R3)	N	A, B, C
	2.4pF (2R4)	N	A, B, C
	2.5pF (2R5)	N	A, B, C
	2.6pF (2R6)	N	A, B, C
	2.7pF (2R7)	N	A, B, C
	2.8pF (2R8)	N	A, B, C
	2.9pF (2R9)	N	A, B, C
	3.0pF (3R0)	N	A, B, C

<sup>1.</sup> The letter in cell is expressed the symbol of product thickness.

<sup>2.</sup> For more information about products with special capacitance or other data, please contact WTC local representative.



DIELECTRIC		NP0		
SIZE	0505	0603	0805	
RATED VOLTAGE (VDC)	250	250	250	Tolerance
0.3pF (0R3)		S	Т	В
0.4pF (0R4)	J	S	T	В
0.5pF (0R5)	J	S	T	A, B, C
0.6pF (0R6)	J	S	Т	A, B, C
0.7pF (0R7)	J	S	T	A, B, C
0.8pF (0R8)	J	S	T	A, B, C
0.9pF (0R9)	J	S	<u>T</u>	A, B, C
1.0pF (1R0)	J	S	<u>T</u>	A, B, C
1.1pF (1R1)	J	S	<u>T</u>	A, B, C
1.2pF (1R2)	J	S S	<u>T</u>	A, B, C
1.3pF (1R3) 1.4pF (1R4)	J 	S	<u>г</u> Т	A, B, C A, B, C
1.5pF (1R5)	<u>J</u>	S	<u>'</u> T	A, B, C
1.6pF (1R6)	<u>J</u>	S	<u>'</u> T	A, B, C
1.7pF (1R7)	J	S	T T	A, B, C
1.8pF (1R8)	J	S	T T	A, B, C
1.9pF (1R9)	J	S	T T	A, B, C
2.0pF (2R0)	J	S	T	A, B, C
2.1pF (2R1)	J	S	T	A, B, C
2.2pF (2R2)	J	S	Т	A, B, C
2.3pF (2R3)	J	S	T	A, B, C
2.4pF (2R4)	J	S	T	A, B, C
2.5pF (2R5)	J	S	<u>T</u>	A, B, C
2.6pF (2R6)	J	S	<u>T</u>	A, B, C
2.7pF (2R7)	J	S	<u>T</u>	A, B, C
2.8pF (2R8)	J	S	<u>T</u>	A, B, C
2.9pF (2R9)	J 	S S	<u>Т</u> Т	A, B, C
3.0pF (3R0) 3.1pF (3R1) 3.2pF (3R2) 3.3pF (3R3)	J	S	<u>'</u>	A, B, C A, B, C
3.1pF (3R1)	<u>J</u>	S	<u>'</u> T	A, B, C
3.3pF (3R2)	<u>J</u>	S	T T	A, B, C
3.4pF (3R4)	J	S	T T	A, B, C
3.5pF (3R5)	J	S	T	A, B, C
3.6pF (3R6)	J	S	T	A, B, C
3.7pF (3R7)	J	S	T	A, B, C
3.8pF (3R8)	J	S	T	A, B, C
3.9pF (3R9)	J	S	T	A, B, C
4.0pF (4R0)	J	S	Т	A, B, C
4.1pF (4R1)	J	S	T	A, B, C
4.2pF (4R2)	J	S	<u>T</u>	A, B, C
4.3pF (4R3)	J	S	<u>T</u>	A, B, C
4.4pF (4R4)	J	S	<u>T</u>	A, B, C
4.5pF (4R5)	J	S	<u>T</u>	A, B, C
4.6pF (4R6)	J	S S	T T	A, B, C
4.7pF (4R7) 4.8pF (4R8)	J 	S S	<u>г</u> Т	A, B, C A, B, C
4.9pF (4R9)		S	<u>'</u> T	A, B, C
5.0pF (5R0)	<u>J</u>	S	<u>'</u> T	A, B, C
5.1pF (5R1)	J	S	Ť	B, C
5.2pF (5R2)	J	S	T	B, C
5.3pF (5R3)	J	S	Ť	В, С
5.4pF (5R4)	J	S	Т	B, C
5.5pF (5R5)	J	S	T	B, C
5.6pF (5R6)	J	S	Т	B, C
5.7pF (5R7)	J	S	Т	B, C
5.8pF (5R8)	J	S	T	B, C
5.9pF (5R9)	J	S	T	B, C
6.0pF (6R0)	J	S	Т	B, C

<sup>1.</sup> The letter in cell is expressed the symbol of product thickness.

<sup>2.</sup> For more information about products with special capacitance or other data, please contact WTC local representative.



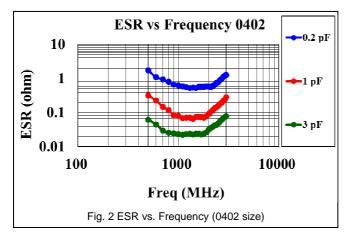
DIELECTRIC					
SIZE		0505	0603	0805	
RATED VOLTAGE (VDC)		250	250	250	Tolerance
	6.1pF (6R1)	J	S	Т	B, C
	6.2pF (6R2)	J	S	Т	B, C
	6.3pF (6R3)	J	S	Т	B, C
	6.4pF (6R4)	J	S	Т	B, C
	6.5pF (6R5)	J	S	Т	B, C
	6.6pF (6R6)	J	S	T	B, C
	6.7pF (6R7)	J	S	T	B, C
	6.8pF (6R8)	J	S	T	B, C
	6.9pF (6R9)	J	S	T	B, C
	7.0pF (7R0)	J	S	T	B, C
	7.1pF (7R1)	J	S	T	B, C
	7.2pF (7R2)	J	S	T	B, C
	7.3pF (7R3)	J	S	T	B, C
	7.4pF (7R4)	J	S	T	B, C
	7.5pF (7R5)	J	S	T	B, C
	7.6pF (7R6)	J	S	T	B, C
	7.7pF (7R7)	J	S	T	B, C
ě	7.8pF (7R8)	J	S	T	B, C
Iĕ	7.9pF (7R9)	J	S	T	B, C
뱚	8.0pF (8R0)	J	S	T	B, C
ğ	8.1pF (8R1)	J	S	T	B, C
Capacitance	8.2pF (8R2)	J	S	T	B, C
ပ	8.3pF (8R3)	J	S	T	B, C
	8.4pF (8R4)	J	S	T	B, C
	8.5pF (8R5)	J	S	T	B, C
	8.6pF (8R6)	J	S	T	B, C
	8.7pF (8R7)	J	S	T	B, C
	8.8pF (8R8)	J	S	T	B, C
	8.9pF (8R9)	J	S	T	B, C
	9.0pF (9R0)	J	S	T	B, C
	9.1pF (9R1)	J	S	<u>T</u>	B, C
	9.2pF (9R2)	J	S	T	B, C
	9.3pF (9R3)	J	S	T	B, C
	9.4pF (9R4)	J	S	<u>T</u>	B, C
	9.5pF (9R5)	J	S	<u>T</u>	B, C
	9.6pF (9R6)	J	S	<u>T</u>	B, C
	9.7pF (9R7)	J	S	<u>T</u>	B, C
	9.8pF (9R8)	J	S	<u>T</u>	B, C
	9.9pF (9R9)	J	S	<u>T</u>	B, C
	10pF (100)	J	S	T	F, G, J

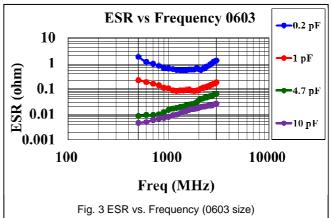
<sup>1.</sup> The letter in cell is expressed the symbol of product thickness.

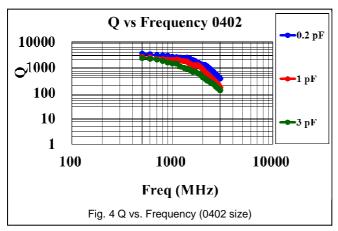
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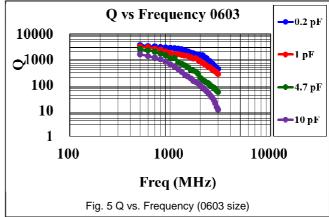


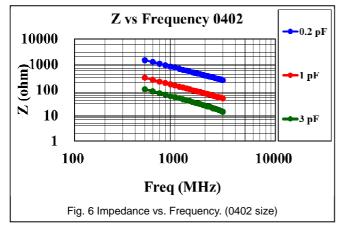
# 9. ELECTRICAL CHARACTERISTICS

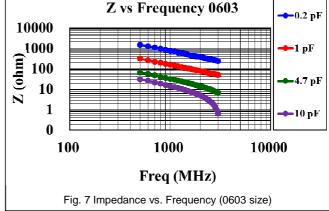




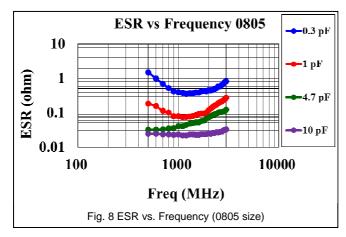


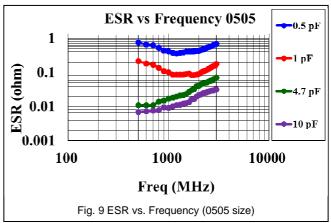


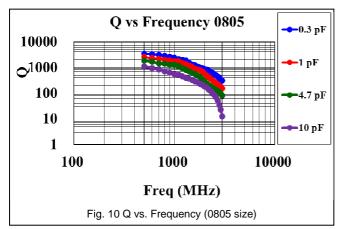


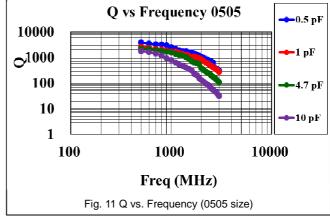


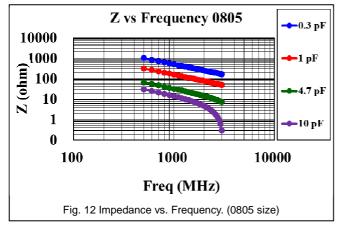


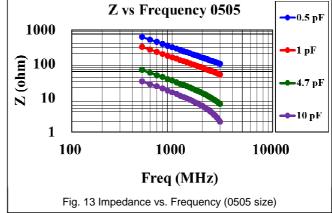














# **10. RELIABILITY TEST CONDITIONS AND REQUIREMENTS**

No.	ltem	Test Conditions	Requirements
1.	Visual and		* No remarkable defect.
	Mechanical		* Dimensions to conform to individual specification sheet.
2.	Capacitance	1.0±0.2Vrms, 1MHz±10%	* Shall not exceed the limits given in the detailed spec.
3.	-	At 25℃ ambient temperature.	* Q≥800+20C
<u> </u>	(Dissipation	'	
	Factor)		
_		**	
4.	Dielectric	* To apply voltage: 200V ~ 250V : 200% of rated voltage.	* No evidence of damage or flash over during test.
	Strength	* Duration: 1 to 5 sec.	
		* Charge & discharge current less than 50mA.	
5.	Insulation	≤100V : To apply rated voltage for max. 120 sec.	≥10GΩ or RxC≥100Ω-F whichever is smaller
	Resistance	≥200V :To apply rated voltage (500V max.) for 60 sec.	
6.	Temperature	With no electrical load.	* Capacitance change: within ±30ppm/℃;
	Coefficient	Operating temperature: -55~150℃ at 25℃	
7.	Adhesive	* Pressurizing force :	* No remarkable damage or removal of the terminations.
	Strength of	0402 to 0603: 5N	
	Termination	>0603: 10N	
		* Test time: 10±1 sec.	
8.	Vibration	* Vibration frequency: 10~55 Hz/min.	* No remarkable damage.
	Resistance	* Total amplitude: 1.5mm	* Cap change and Q/D.F.: To meet initial spec.
		* Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)	
		* Cap./DF(Q) Measurement to be made after de-aging at	
		150°C for 1hr then set for 24±2 hrs at room temp.	
9.	Solderability	* Solder temperature: 235±5℃	95% min. coverage of all metalized area.
		* Dipping time: 2±0.5 sec.	
10.	Bending Test	* The middle part of substrate shall be pressurized by means	* No remarkable damage.
		of the pressurizing rod at a rate of about 1 mm per second until	* Cap change: within ±5.0% or ±0.5pF whichever is larger.
		the deflection becomes 1 mm and then the pressure shall be maintained for 5±1 sec.	(This capacitance change means the change of capacitance under
		* Measurement to be made after keeping at room temp. for	specified flexure of substrate from the capacitance measured before
		24±2 hrs.	the test.)
11.	Resistance to	* Solder temperature: 260±5℃	* No remarkable damage.
	Soldering Heat	* Dipping time: 10±1 sec	* Cap change: within ±2.5% or ±0.25pF whichever is larger.
		* Preheating: 120 to 150°C for 1 minute before imme rse the	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.
		capacitor in a eutectic solder.  * Cap. / DF(Q) / I.R. Measurement to be made after de-aging	* 25% max. leaching on each edge.
		at 150°C for 1hr then set for 24±2 hrs at room temp.	
		at 150 C for the their set for 2412 his at 160m temp.	E

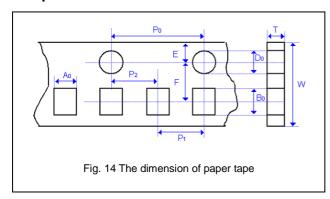


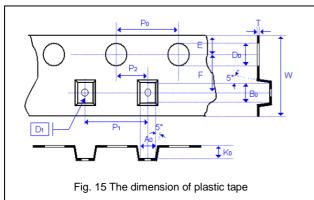
No.	Item		Test Condition		Requi	rements	
12.	Temperature Cycle	* Conduct	the five cycles according to the ter	mperatures and	* No remarkable damage. * Cap change: within ±2.5% or	±0.25pF whichever is larger.	
13.	Humidity (Damp Heat)	Step  1 2 3 4 *Before in at 150°C for * Cap. / Di 150°C for * Test tem * Humidity	Temp. (°C)  Min. operating temp. +0/-3  Room temp.  Max. operating temp. +3/-0  Room temp.  itial measurement (Class II only): Tor 1hr then set for 24±2 hrs at room F(Q) / I.R. Measurement to be mad 1hr then set for 24±2 hrs at room to 1p.: 40±2°C  7: 90~95% RH	n temp . de after de-aging at	* Q/D.F., I.R. and dielectric strer	ngth: To meet initial requirements.	
	Steady State	* Test time: 500+24/-0hrs.  * Cap. / DF(Q) / I.R. Measurement to be made after de-aging a 150°C for 1hr then set for 24±2 hrs at room temp.			* O/D F_value: 10nF <can<30nf_o>275+2 5C</can<30nf_o>		
14.	Humidity (Damp Heat) Load	* Test temp.: 40±2°C  * Humidity: 90~95%RH  * Test time: 500+24/-0 hrs.  * To apply voltage: rated voltage (MAX. 500V)  * Cap. / DF(Q) / I.R. Measurement to be made after de-aging a			* No remarkable damage.  * Cap change: within ±7.5% or ±0.75pF whichever is larger.  * Q/D.F. value: Cap<30pF, Q≥100+10/3C  g at * I.R.: ≥500MΩ.		
15.	High Temperature Load (Endurance)	* Test temp.: 150±3℃  * To apply voltage:  10V ≦Ur<500V: 200% of rated voltage.  * Test time: 2000+24/-0 hrs.  * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150℃ for 1hr then set for 24±2 hrs at room temp.			* No remarkable damage. * Cap change: within ±3.0% or ± * Q/D.F. value: Cap≤10pF, Q≥20 * I.R.: ≥1GΩ.	· =	
16.	ESR		should be measured at room temp	erature and tested	0402 0.1pF≤Cap≤1pF:< 330mΩ/pF 1pF <cap≤3pf:< 0.3pf≤cap≤1pf:<="" 0603="" 1400mω="" 1pf<cap≤10pf:<="" 230mω<="" 280mω="" th=""><th>0505 0.4pF≤Cap&lt;1.0pF: &lt; 1400mΩ 1.0pF≤Cap≤10pF: &lt; 230mΩ 0805 0.3pF≤Cap≤1pF: &lt; 1400mΩ 1pF<cap≤10pf: 230mω<="" <="" th=""></cap≤10pf:></th></cap≤3pf:<>	0505 0.4pF≤Cap<1.0pF: < 1400mΩ 1.0pF≤Cap≤10pF: < 230mΩ 0805 0.3pF≤Cap≤1pF: < 1400mΩ 1pF <cap≤10pf: 230mω<="" <="" th=""></cap≤10pf:>	

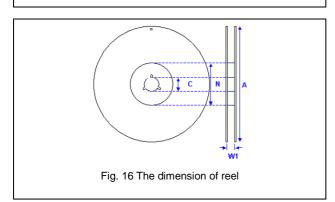


## **APPENDIXES**

# **■ Tape & reel dimensions**



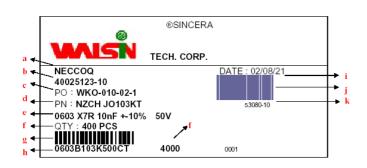




Size	0402	0505	0603	0805
Thickness	N	J	S	Т
<b>A</b> <sub>0</sub>	0.70 +/-0.20	< 1.90	1.05 +/-0.30	1.50 +/-0.20
B <sub>0</sub>	1.20 +/-0.20	< 1.90	1.80 +/-0.30	2.30 +/-0.20
Т	≦0.80	0.23 +/-0.1	≦1.20	≦1.20
K <sub>0</sub>	=	< 1.50	=	-
w	8.00	8.00	8.00	8.00
	+/-0.30	+/-0.30	+/-0.30	+/-0.30
P <sub>0</sub>	4.00	4.00	4.00	4.00
	+/-0.10	+/-0.10	+/-0.10	+/-0.10
10xP <sub>0</sub>	40.00	40.00	40.00	40.00
	+/-0.10	+/-0.20	+/-0.20	+/-0.20
P <sub>1</sub>	2.00	4.00	4.00	4.00
	+/-0.05	+/-0.10	+/-0.10	+/-0.10
P <sub>2</sub>	2.00	2.00	2.00	2.00
	+/-0.05	+/-0.05	+/-0.05	+/-0.05
D <sub>0</sub>	1.50	1.50	1.50	1.50
	+0.1/-0	+0.1/-0	+0.1/-0	+0.1/-0
<b>D</b> <sub>1</sub>	-	1.00 +/-0.10	-	-
E	1.75	1.75	1.75	1.75
	+/-0.10	+/-0.10	+/-0.10	+/-0.10
F	3.50	3.50	3.50	3.50
	+/-0.05	+/-0.05	+/-0.05	+/-0.05

Size	0402, 0505, 0603, 0805		
Reel size	7"	13"	
С	13.0+0.5/-0.2	13.0+0.5/-0.2	
$\mathbf{W}_1$	8.4+1.5/-0	8.4+1.5/-0	
Α	178.0±1.0	330.0±1.0	
N	60.0+1.0/-0	100±1.0	

# **■** Description of customer label

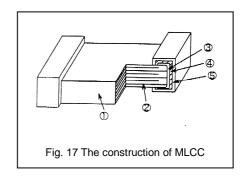


- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label



#### Constructions

No.	Na	NP0	
①	Ceramic material		BaTiO₃ based
2	Inner electrode		Cu
3		Inner layer	Cu
4	Termination	Middle layer	Ni
(5)		Outer layer	Sn (Matt)



## Storage and handling conditions

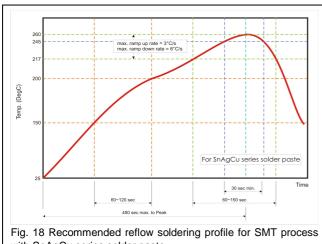
- (1) To store products at 5 to 40°C ambient temperature and 20 to 70%. related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

#### Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

#### Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N<sub>2</sub> within oven are recommended.



with SnAgCu series solder paste.

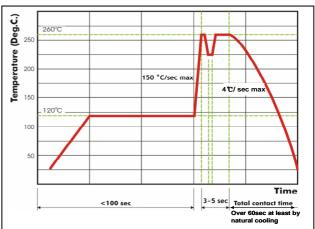


Fig. 19 Recommended wave soldering profile for SMT process with SnAgCu series solder.